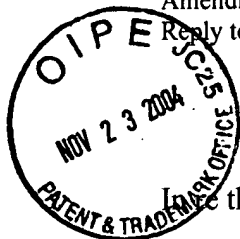


Appln. No.: 09/028,276
Amendment dated November 23, 2004
Reply to Office Action of August 25, 2004



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Shigeru ATSUMI

Serial No.: 09/028,276

Filed: February 24, 1998

For: Semiconductor Integrated Circuit Device and
Flash EEPROM

Atty. Docket No.: 001701.73982

Group Art Unit: 2815

Examiner: Eckert II, G.

Confirmation No.: 4461

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AMENDMENT

U.S. Patent and Trademark Office
220 20th Street S.
Customer Window, Mail Stop Amendment
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Sir:

In response to the Office Action mailed August 25, 2004, please amend the instant application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the Listing of Claims, which begins on page 3 of this paper.

Amendments to the Drawings begin on page 10 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 11 of this paper.

An **Appendix** including amended drawing figures is attached following page 12 of this paper.